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This unscheduled ATM has been written to define in more detail the test programs to be employed during system integration.

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In proposing the following test programs, two assumptions have been made; firstly, that the central station has been integrated using the Data Subsystem Test Set and tested as a subsystem using the System Test Set; and secondly, that each experiment has been individually tested using its Experiment Test Set.

Starting with the central station and the System Test Set, one experiment is connected to the central station via its break-out box in place of the appropriate S.T.S. experiment simulator. This experiment is then tested using a test sequence which is very similar to the Integrated System Test Sequence for that experiment together with an inspection of the central station/experiment electrical interface. This experiment is then put on 'standby', and the next experiment is connected and tested in similar fashion. This sequence of events is repeated until all experiments have been connected and tested. These test sequences will be referred to as the "Experiment Integration Test Sequences."

After completion of the Experiment Integration Test Sequences, all experiments are switched 'on' and set in their basic mode of operation. The central station housekeeping data output, all experiment scientific, engineering and electrical interfaces are examined for evidence of cross-talk from other experiments. Then each experiment in turn has all its commands exercised while all other experiment outputs and interfaces, and the central station housekeeping data output are examined for cross-talk from the commands and the results of command execution. Finally, the central station commands are exercised while all experiment outputs and interfaces are examined for cross-talk.

Subsequent revisions of this ATM will contain the detailed test schedules outlined above.