Compact Ultra Low Temperature Instrumentation for the Lunar Surface

P.E. Clark1, S. Whitaker2, G. Maki2, K. Brown3, R. Cox4

1Catholic University of America, 2ICs, 3Morehead State University, 4Flexure Engineering

Contact: Pamela.E.Clark@NASA.gov

Context

The single most important discovery in lunar science in the last decade is the confirmation of icy volatiles at the lunar poles. This discovery implies that volatiles are far more ubiquitous on most of the surfaces in the solar system, for which the lunar polar regions are an analogue, than previously assumed. Apparently, the solar system still has far more to reveal about its origin, history, and current state. This not only makes the Moon an exciting destination in its own right, but the Lunar polar regions a Rosetta Stone for cryogenic chemistry and physics throughout the solar system and beyond, and a testbed for technologies needed to support exploration of the entire solar system.

How it Started

Two years ago, we began a multi-year effort to develop strategies and design concepts for ALSEP-like stand-alone lunar surface instrument packages with minimized mass/power requirements and without dependence on radioisotope-based batteries. We used a state of the art lunar environmental monitoring station (LEMS) that would be operational at all times at the poles for a minimum of five years, as our model. An initial conventional attempt to design an environmental monitoring package with a solar/battery based power system led to a package with an acceptably large mass (500 kg) of which over half was battery mass. Our Phase 1 support allowed us to design a package with considerable reduction (5x to 100 kg) in the initial mass of such a package deployable near the poles (up to a few days of darkness once a year) by incorporating a) radiation hard, cold temperature electronics readily available but not routinely considered for deep space missions and b) innovative thermal balance strategies through use of multi-layer thin materials and gravity-assisted heat pipes.

Purpose

Here, we discuss technologies needed to support such exploration of the lunar polar regions, their current state of and ongoing activities related to their development, particularly in regard to cold temperature electronics. A particular concern operation under the colder and darker conditions at or near the lunar poles, particularly in the always or nearly always shadowed regions that can act as efficient cold traps for such volatiles.

Technology Challenges: Thermal, Mechanical, Mechanisms

Thermal and mechanical design must provide a shield from extreme environmental conditions as well as deep space radiation on the lunar surface. Clark and coworkers developed a concept for a passively temperature controlled instrument package operating on a limited duty cycle using JWST G10 material as insulation, a radiator with latitude dependent alignment, and a gravity-assisted heat pipe to greatly reduce heat loss during lunar night. NASA recently developed polymer aerogel (no longer brittle) material would provide even better insulation with even less mass penalty and allow an expanded duty cycle. High temperature superconductors could also provide the basis for efficient mechanisms for applications where ‘moving parts’ are required to operate under cryogenic conditions where minimal power is available, as well as for magnetic shields to protect equipment or crew in deep space radiation environments. Such HTS-based concepts have been designed and tested.
Technology Challenges: Power

Designing power systems to operate at cold temperatures is especially challenging. Hibernation has been proposed to provide limited duty cycle operation on the lunar surface. Li battery technology has been undergoing systematic improvement at JPL through the use of electrolytes that can carry charge at far lower temperatures than conventional batteries. Indications are that Li-based battery technology will allow operation down to -100 degrees C within the next few years. Recently, solvent-in-salt electrolytes have been studied and shown to enhance charge/discharge and safety performance.

For operating at the extremely cold temperatures in the permanently shadowed areas on the Moon, down to 25K, we will need high temperature superconductor systems now under development by Selvamanickam, Masson, Beno, Meinke, and others. High Temperature Superconductor based systems for cooling, power generation, power transmission, energy storage and regulation (superconducting magnetic energy storage or flywheels) are currently being developed. Indeed, for the large-scale applications for efficient power generation, but we will require the same scales that are normally used in the laboratory. HTS-based technologies, although currently relatively low TRL, would provide optimal solutions for operating at cold temperatures.

Technology Challenges: Avionics, Electronics

Cold temperature analogue (Si Ge) [8] and digital (ULT ULP) electronics that will currently operate at these ‘cryo’ temperatures far more efficiently and with lower noise than conventional electronics are available now, and awaiting an opportunity to be employed in the design of an entire package, which may need to be designed to operate at a lower voltage. ULT ULP chips demonstrated orders of magnitude savings in power consumption and thermal tolerance (Maki and Yeh, 2003) on CULPRIT (CMOS Ultra-low Power Radiation Tolerant) flown on NASA ST5 (2006), similar high end channel coder and compression chips requested for use in MMS, GOES-R missions. Currently, CAMBR (Center for Advanced Microelectronics and Bio-molecular Research), Idaho team with U. Michigan, completed rad-hard-by-design (RHBD) test chip for 90nm, as well as low-power cross-correlator chip capable of performing over 10,000 correlations simultaneously at 800 Mhz for ESTO (Instrument Incubator Program) instrument project (GEOSTAR) with JPL lead PI.

Recent development in low-power rad-hard electronics involves smaller CMOS feature sizes, currently down to 90nm and 65nm. At these sizes, the core voltage of chip in range of 1.0V, a 25:1 power reduction compared to earlier 5.0V components. Though ULP likely more energy efficient (by factor of 4), requires access to fabrication process (e.g., change doping recipe) not as readily available. 90 or 65nm may offer alternative solution to ULP electronics.

Applications

We have proposed to target onboard avionics needs of deep space autonomous systems, including communications as well as command, control, and data handling, through development of a general purpose, radiation hardened by design (RHBD) processor that can also serve as a programmable controller for varied applications, as applied to a specific instantiation: LunarCube, the very low-mass, low-power, and low-cost yet capable standardized spacecraft bus concept that has enabled the highly successful CubeSat model for deep space operation. The current concept leverages the Morehead State CXBN mission 3U bus [12], capable of accommodating a single guest instrument, to be self- or human-deployed on the lunar surface.

The most challenging surface activities which we will ultimately address include measurement and curator of cryogenic samples, robotic rover operation at or near cold traps, combined in situ measurement techniques in lieu of sample handling techniques, and distributed, self-deploying astrophysical observatory networks, such as ROLSS.