



Key Question:

Is The Transient Response Affected by TID Exposure (SiGe HBT + CMOS)?

Device Test Structures:

- GlobalFoundries' 8HP SiGe BiCMOS
- npn SiGe HBT ($0.12 \times 0.58 \mu\text{m}^2$)
- nFET ($W/L=10/0.12 \mu\text{m}$)
- Biased to worst case conditions for TID

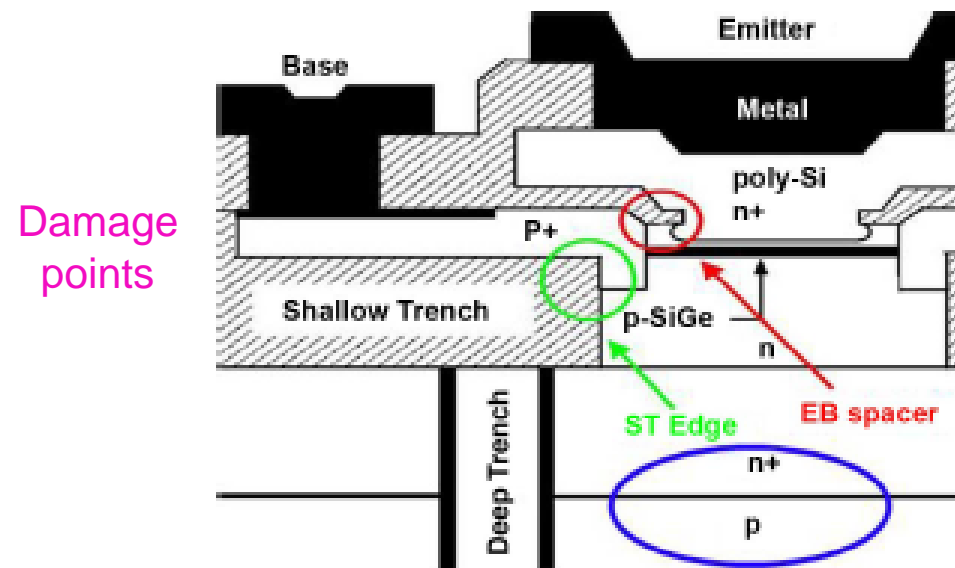
Experimental Details:

- Total Ionizing Dose (NRL)
 - ARACOR 10-keV X-ray source
- Pulsed-Laser TPA for SET (NRL)
 - Two Photon Absorption laser source

TCAD Modeling (Sentaurus):

- Illustrate leakage mechanism

2D NPN cross-section of SiGe HBT



eDensity during Heavy Ion Strike Simulation

